

Mitsubishi Programmable Controller

MELSEC iQ-R
series

MELSEC iQ-R CPU Module Function Block Reference

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1 FUNCTION BLOCK (FB) LIST

This chapter lists the FBs for the MELSEC iQ-R series CPU module.

FBs for the Ethernet function

For the Ethernet function FBs of CPU modules, refer to the "Ethernet-EQUIPPED MODULE FB" in the following.

 MELSEC iQ-R Ethernet/CC-Link IE Function Block Reference

FBs for the inter-module synchronization function

Name ^{*1}	Description
M+RCPU_MSynchronization_Delay1OUT M+RCPU_MSynchronization_Delay2OUT	M+RCPU_MSynchronization_Delay1OUT holds the value specified by input data (output request data) in the FB and executes the OUT instruction when the FB is called next. M+RCPU_MSynchronization_Delay2OUT executes the OUT instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1SET M+RCPU_MSynchronization_Delay2SET	M+RCPU_MSynchronization_Delay1SET memorizes that this function has been called by the FB and executes the SET instruction when the FB is called next. M+RCPU_MSynchronization_Delay2SET executes the SET instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1RST M+RCPU_MSynchronization_Delay2RST	M+RCPU_MSynchronization_Delay1RST memorizes that this function has been called by the FB and executes the RST instruction when the FB is called next. M+RCPU_MSynchronization_Delay2RST executes the RST instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1MOV M+RCPU_MSynchronization_Delay2MOV	M+RCPU_MSynchronization_Delay1MOV holds the value specified by input data in the FB and executes the MOV instruction when the FB is called next. M+RCPU_MSynchronization_Delay2MOV executes the MOV instruction when the FB is called second time.
M+RCPU_MSynchronization_Delay1DMOV M+RCPU_MSynchronization_Delay2DMOV	M+RCPU_MSynchronization_Delay1DMOV holds the value specified by input data in the FB and executes the DMOV instruction when the FB is called next. M+RCPU_MSynchronization_Delay2DMOV executes the DMOV instruction when the FB is called second time.

*1 Note that this reference does not describe the FB version information which is displayed such as "_00A" at the end of FB name

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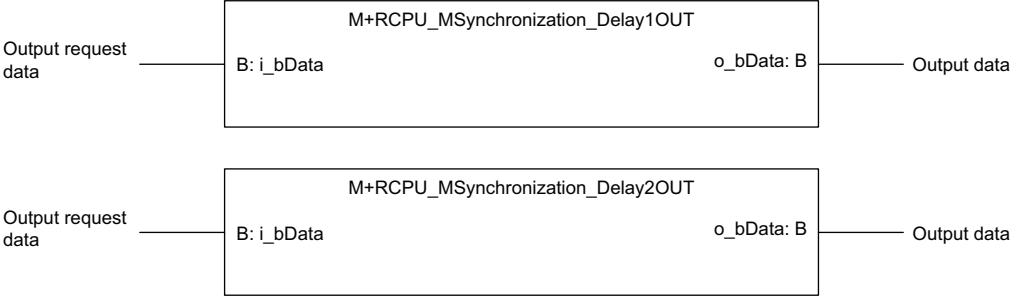
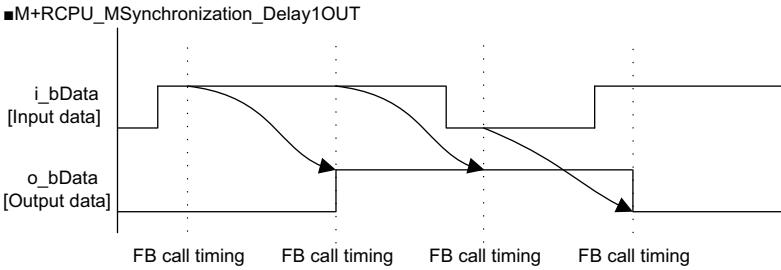
2 CPU MODULE FB

2.1 M+RCPU_MSynchronization_Delay1OUT, M+RCPU_MSynchronization_Delay2OUT

Name

M+RCPU_MSynchronization_Delay1OUT, M+RCPU_MSynchronization_Delay2OUT

FB details

Item	Description	
Overview	Holds the value specified by output request data (input data) in the FB and executes the OUT instruction when the FB is called next or the second time.	
Symbol		
Available device	CPU module	RCPU
	Engineering tool	GX Works3
Language	Ladder diagram	
Number of basic steps	12 steps	
Processing	Holds the output request data (bit) specified by i_bData in the FB and outputs it to o_bData when the FB is called next or the second time. M+RCPU_MSynchronization_Delay1OUT outputs data when the FB is called next. M+RCPU_MSynchronization_Delay2OUT outputs data when the FB is called second time.	
FB compilation method	Macro type	
FB operation	Always executed	
Input condition for FB_EN	None	
Timing chart of I/O signals	 <p>* M+RCPU_MSynchronization_Delay2OUT outputs data at the second FB call timing after FB is once called.</p>	
Restrictions or precautions	<ul style="list-style-type: none">Always outputs OFF by the number of Delays after STOP changes to RUN.	

Error code

There is no error code.

Labels

■Input label

Name	Variable name	Data type	Range	Default value	Description
Output request data	i_bData	Bit	On or off	None	Specifies output data. On: Requesting output on Off: Requesting output off

■Output label

Name	Variable name	Data type	Range	Description
Output data	o_bData	Bit	On or off	Output data are set.

2.2 M+RCPU_MSynchronization_Delay1SET, M+RCPU_MSynchronization_Delay2SET

Name					
M+RCPU_MSynchronization_Delay1SET, M+RCPU_MSynchronization_Delay2SET					
FB details					
Item	Description				
Overview	Memorizes that this function has been called by the FB and executes the SET instruction when the FB is called next or the second time.				
Symbol	<pre> graph TD subgraph "M+RCPU_MSynchronization_Delay1SET" direction TB A1[Execution command] --> B1[B: i_bEN] B1 --> C1[o_bData: B] C1 --> D1[Output data] end subgraph "M+RCPU_MSynchronization_Delay2SET" direction TB A2[Execution command] --> B2[B: i_bEN] B2 --> C2[o_bData: B] C2 --> D2[Output data] end </pre>				
Available device	<table border="1"> <tr> <td>CPU module</td> <td>RCPU</td> </tr> <tr> <td>Engineering tool</td> <td>GX Works3</td> </tr> </table>	CPU module	RCPU	Engineering tool	GX Works3
CPU module	RCPU				
Engineering tool	GX Works3				
Language	Ladder diagram				
Number of basic steps	12 steps				
Processing	When i_bEN is turned on, the function holds the state in the FB and sets it in o_bData when the FB is called next or the second time. M+RCPU_MSynchronization_Delay1SET is set when the FB is called next. M+RCPU_MSynchronization_Delay2SET is set when the FB is called second time.				
FB compilation method	Macro type				
FB operation	Always executed				
Input condition for FB_EN	None				
Timing chart of I/O signals	<p>M+RCPU_MSynchronization_Delay1SET</p> <p>i_bEN [Execution command]</p> <p>o_bData [Output data]</p> <p>FB call timing FB call timing</p> <p>* M+RCPU_MSynchronization_Delay2SET sets the device at the second FB call timing after FB is once called.</p>				
Restrictions or precautions	<ul style="list-style-type: none"> Holds the output by the number of Delays after STOP changes to RUN. 				

Error code

There is no error code.

Labels

■Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.

■Output label

Name	Variable name	Data type	Range	Description
Output data	o_bData	Bit	On or off	Output data are set.

2.3 M+RCPU_MSynchronization_Delay1RST, M+RCPU_MSynchronization_Delay2RST

Name					
M+RCPU_MSynchronization_Delay1RST, M+RCPU_MSynchronization_Delay2RST					
FB details					
Item	Description				
Overview	Memorizes that this function has been called by the FB and executes the RST instruction when the FB is called next or the second time.				
Symbol	<p>The symbol shows two function blocks side-by-side. Both blocks have an input labeled "Execution command" and an output labeled "Output data". The top block is labeled "M+RCPU_MSynchronization_Delay1RST" and the bottom block is labeled "M+RCPU_MSynchronization_Delay2RST". Both blocks also have an input labeled "B: i_bEN".</p>				
Available device	<table border="1"> <tr> <td>CPU module</td> <td>RCPU</td> </tr> <tr> <td>Engineering tool</td> <td>GX Works3</td> </tr> </table>	CPU module	RCPU	Engineering tool	GX Works3
CPU module	RCPU				
Engineering tool	GX Works3				
Language	Ladder diagram				
Number of basic steps	12 steps				
Processing	When <i>i_bEN</i> is turned on, the function holds the state in the FB and resets <i>o_bData</i> when the FB is called next or the second time. M+RCPU_MSynchronization_Delay1RST resets the device when the FB is called next. M+RCPU_MSynchronization_Delay2RST resets the device when the FB is called second time.				
FB compilation method	Macro type				
FB operation	Always executed				
Input condition for FB_EN	None				
Timing chart of I/O signals	<p>M+RCPU_MSynchronization_Delay1RST</p> <p>The timing chart shows two signals: <i>i_bEN</i> [Execution command] and <i>o_bData</i> [Output data]. The <i>i_bEN</i> signal is a pulse that triggers the function. The <i>o_bData</i> signal is a waveform that changes state at the end of the <i>i_bEN</i> pulse. Two vertical dashed lines indicate "FB call timing".</p> <p>* M+RCPU_MSynchronization_Delay2RST resets the device at the second FB call timing after FB is once called.</p>				
Restrictions or precautions	<ul style="list-style-type: none"> Holds the output by the number of Delays after STOP changes to RUN. 				

Error code

There is no error code.

Labels

■Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.

■Output label

Name	Variable name	Data type	Range	Description
Output data	o_bData	Bit	On or off	Output data are set.

2.4 M+RCPU_MSynchronization_Delay1MOV, M+RCPU_MSynchronization_Delay2MOV

Name

M+RCPU_MSynchronization_Delay1MOV, M+RCPU_MSynchronization_Delay2MOV

FB details

Item	Description	
Overview	Holds the value specified by transfer source data (input data) in the FB and executes the MOV instruction when the FB is called next or the second time.	
Symbol	<p>The symbol shows two identical function blocks side-by-side. Each block has an 'Execution command' input (labeled B: i_bEN) and a 'Transfer source data' input (labeled W: i_wData). The output is labeled o_wData: W. A bracket indicates that both blocks represent the same function: M+RCPU_MSynchronization_Delay1MOV or M+RCPU_MSynchronization_Delay2MOV.</p>	
Available device	CPU module	RCPU
	Engineering tool	GX Works3
Language	Ladder diagram	
Number of basic steps	<ul style="list-style-type: none"> • 18 steps (M+RCPU_MSynchronization_Delay1MOV) • 20 steps (M+RCPU_MSynchronization_Delay2MOV) 	
Processing	<p>When i_bEN is turned on, the function transfers the data specified by i_wData to o_wData in 16-bit data transfer mode when the FB is called next or the second time.</p> <p>M+RCPU_MSynchronization_Delay1MOV transfers data when the FB is called next.</p> <p>M+RCPU_MSynchronization_Delay2MOV transfers data when the FB is called second time.</p>	
FB compilation method	Macro type	
FB operation	Always executed	
Input condition for FB_EN	None	
Timing chart of I/O signals	<p>M+RCPU_MSynchronization_Delay1MOV</p> <p>The timing chart illustrates the signal behavior for M+RCPU_MSynchronization_Delay1MOV. It shows three cycles labeled A, B, and C. In each cycle, the execution command (i_bEN) is asserted at the start of the first FB call timing. The transfer source data (i_wData) is present during the first FB call timing, and the transfer destination data (o_wData) is updated during the same period. Arrows indicate the data flow from i_wData to o_wData.</p> <p>* M+RCPU_MSynchronization_Delay2MOV transfers data at the second FB calling after FB is once called.</p>	
Restrictions or precautions	<ul style="list-style-type: none"> • Holds the output by the number of Delays after STOP changes to RUN. 	

Error code

There is no error code.

Labels

■Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.
Transfer source data	i_wData	Word [signed]	Effective device range	None	Specify the transfer source data or the device containing the transfer source data.

■Output label

Name	Variable name	Data type	Range	Description
Transfer destination data	o_wData	Word [signed]	Effective device range	Transfer data is stored.

2.5 M+RCPU_MSynchronization_Delay1DMOV, M+RCPU_MSynchronization_Delay2DMOV

Name					
M+RCPU_MSynchronization_Delay1DMOV, M+RCPU_MSynchronization_Delay2DMOV					
FB details					
Item	Description				
Overview	Holds the value specified by transfer source data (input data) in the FB and executes the DMOV instruction when the FB is called next or the second time.				
Symbol	<pre> graph TD subgraph PathA [M+RCPU_MSynchronization_Delay1DMOV] B1[i_bEN] --> A1[i_dData] A1 --> O1[o_dData:D] end subgraph PathB [M+RCPU_MSynchronization_Delay2DMOV] B2[i_bEN] --> A2[i_dData] A2 --> O2[o_dData:D] end </pre>				
Available device	<table border="1"> <tr> <td>CPU module</td><td>RCPU</td></tr> <tr> <td>Engineering tool</td><td>GX Works3</td></tr> </table>	CPU module	RCPU	Engineering tool	GX Works3
CPU module	RCPU				
Engineering tool	GX Works3				
Language	Ladder diagram				
Number of basic steps	<ul style="list-style-type: none"> • 18 steps (M+RCPU_MSynchronization_Delay1DMOV) • 20 steps (M+RCPU_MSynchronization_Delay2DMOV) 				
Processing	When <i>i_bEN</i> is turned on, the function transfers the data specified by <i>i_dData</i> to <i>o_dData</i> in 32-bit data transfer mode when the FB is called next or the second time. M+RCPU_MSynchronization_Delay1DMOV transfers data when the FB is called next. M+RCPU_MSynchronization_Delay2DMOV transfers data when the FB is called second time.				
FB compilation method	Macro type				
FB operation	Always executed				
Input condition for FB_EN	None				
Timing chart of I/O signals	<p>■ M+RCPU_MSynchronization_Delay1DMOV</p> <p>* M+RCPU_MSynchronization_Delay2DMOV transfers data at the second FB calling after FB is once called.</p>				
Restrictions or precautions	<ul style="list-style-type: none"> • Holds the output by the number of Delays after STOP changes to RUN. 				

Error code

There is no error code.

Labels

■Input label

Name	Variable name	Data type	Range	Default value	Description
Execution command	i_bEN	Bit	On or off	None	On: FB starts. Off: FB does not start.
Transfer source data	i_dData	Double word [signed]	Effective device range	None	Specify the transfer source data or the device containing the transfer source data.

■Output label

Name	Variable name	Data type	Range	Description
Transfer destination data	o_dData	Double word [signed]	Effective device range	Transfer data is stored.

INSTRUCTION INDEX

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REVISIONS

*The manual number is given on the bottom left of the back cover.

Revision date	*Manual number	Description
June 2014	BCN-P5999-0374-A	First edition

Japanese manual number: BCN-P5999-0364-A

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