

FMH20N50E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

Maintains both low power loss and low noise Lower R_{DS}(on) characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (3.0±0.5V) High avalanche durability

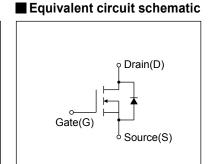
Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

Outline Drawings [mm] TO-3P(Q) + + +



Description	Symbol	Characteristics	Unit	Remarks	
Dania Carras Valtana	V _{DS}	500	V		
Drain-Source Voltage	V _{DSX}	500	V	V _{GS} = -30\	
Continuous Drain Current	In	±20	Α		
Pulsed Drain Current	IDP	±80	Α		
Gate-Source Voltage	V _{GS}	±30	V		
Repetitive and Non-Repetitive Maximum Avalanche Current	Iar	20	А	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	582.5	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	23.5	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	7.4	kV/μs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Maximum Power Dissipation	Po	2.5	14/	Ta=25°C	
		235	W	Tc=25°C	
Operating and Storage	Tch	150	°C		
Temperature range	Teta	-55 to +150	°C		

● Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	BVDSS	I _D =250µA, V _{GS} =0V		500	-	-	V
Gate Threshold Voltage	V _{GS} (th)	In=250µA, Vns=Vs	I _D =250µA, V _{DS} =V _{GS}		3.0	3.5	V
Zero Gate Voltage Drain Current		V _{DS} =500V, V _{GS} =0V	Tch=25°C	-	-	25	μΑ
	Inss	V _{DS} =400V, V _{GS} =0V	T _{ch} =125°C	-	-	250	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA
Drain-Source On-State Resistance	R _{DS} (on)	I _D =10A, V _{GS} =10V		-	0.27	0.31	Ω
Forward Transconductance	g fs	I _D =10A, V _{DS} =25V		11	22	-	S
Input Capacitance	Ciss	Vbs=25V - Vbs=0V - f=1MHz -		-	2650	3980	pF
Output Capacitance	Coss			-	250	375	
Reverse Transfer Capacitance	Crss			19	28.5		
Turn-On Time	td(on)	V _{cc} =300V V _{cS} =10V I _D =10A R _c =10Ω		-	22	33	ns
	tr			-	11	16.5	
Turn-Off Time	td(off)			-	120	180	
	tf			-	21	31.5	
Total Gate Charge	QG	Vcc=250V		-	77	115.5	nC
Gate-Source Charge	Qss	ID=20A	I _D =20A		17	25.5	
Gate-Drain Charge	Q _{GD}	V _{GS} =10V		-	22	33	
Avalanche Capability	lav	L=1.07mH, Tch=25°C		20	-	-	Α
Diode Forward On-Voltage	V _{SD}	I _F =20A, V _{GS} =0V, T _{ch} =25°	I _F =20A, V _{GS} =0V, T _{ch} =25°C		0.90	1.35	V
Reverse Recovery Time	trr	I _F =20A, V _{GS} =0V	I _F =20A, V _{GS} =0V		0.5	-	μs
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	7	-	μC

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to Case			0.532	°C/W
	Rth (ch-a)	Channel to Ambient			50.0	°C/W

Note *1 : Tch≤150°C

Note *2 : Stating Tch=25°C, Ias=8A, L=16.7mH, Vcc=50V, Rg=50 Ω Eas limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

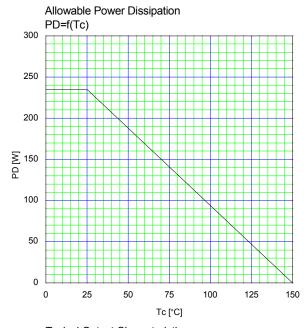
Note *3: Repetitive rating: Pulse width limited by maximum channel temperature

See to the 'Transient Themal impeadance' graph.

Note *4 : I_F≤-I_D, -di/dt=100A/µs, Vcc≤BVbss, Tch≤150°C.

Note *5 : I_F≤-I_D, dv/dt=7.4kV/µs, Vcc≤BVbss, Tch≤150°C.

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10¹ 100µs 100µs

Safe Operating Area

Power loss waveform Square waveform

10°

10²

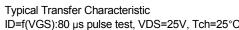
10⁻¹

10⁻²

10⁻¹

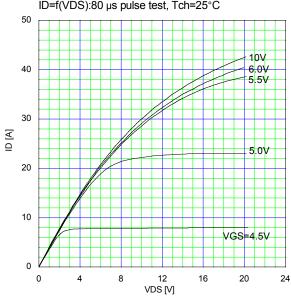
ID=f(VDS):Duty=0(Single pulse), Tc=25°c

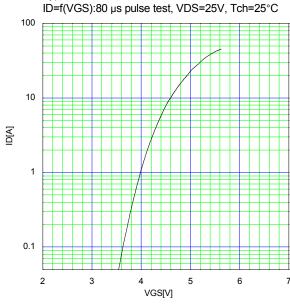
Typical Output Characteristics ID=f(VDS):80 µs pulse test, Tch=25°C



10¹ VDS [V] 10²

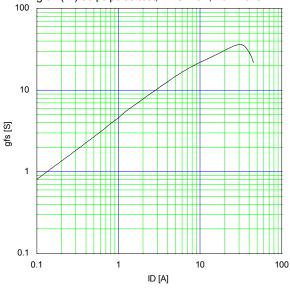
10³

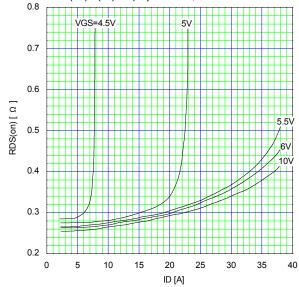


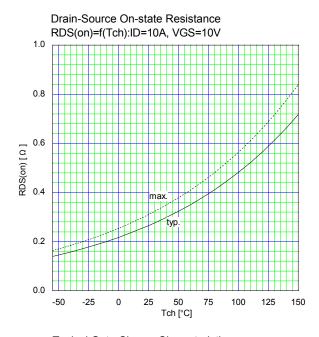


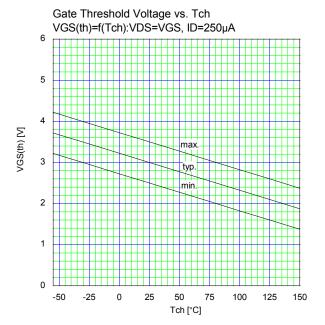
Typical Transconductance gfs=f(ID):80 µs pulse test, VDS=25V, Tch=25°C

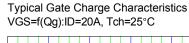
Typical Drain-Source on-state Resistance RDS(on)=f(ID):80 µs pulse test, Tch=25 °C

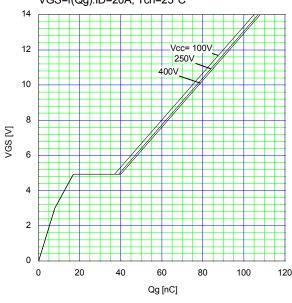


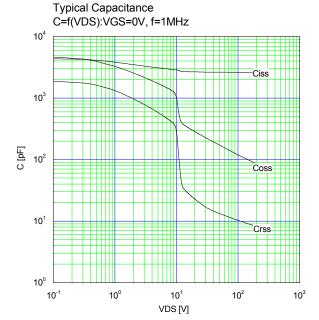




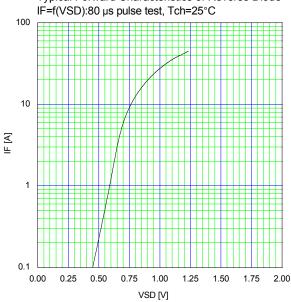


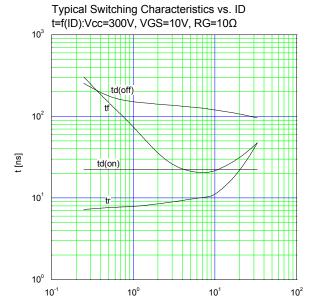






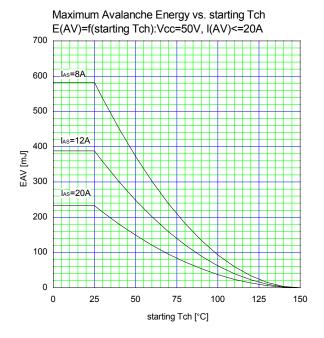
Typical Forward Characteristics of Reverse Diode

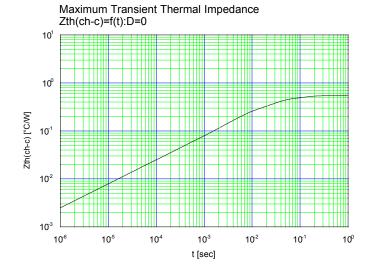




ID [A]

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